Serial No.: *09/763,789*

Office Action dated: November 3, 2005

PATENT RCA 89175

Listing and Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the

application:

1. (currently amended) A method for controlling a video processing apparatus,

the method comprising:

(a) commanding a peripheral device, connected to said video processing

apparatus, to transmit an analog a test signal pattern from an analog output of said

peripheral device;

(b) receiving said analog test signal pattern from said peripheral device on one of

a plurality of analog inputs of said video processing apparatus, wherein said video

processing apparatus is capable of distinguishing said test signal pattern from other

signals received on said analog inputs;

(c) determining enabling said video processing apparatus to determine which

one of said plurality of analog inputs receives said analog test signal is received pattern;

and

(d) storing data, in said video processing apparatus, associated with said analog

input which has received said analog test signal pattern.

2. (currently amended) The method of Claim 1 wherein the step of commanding

comprises sending a message via a digital bus interconnecting said video processing

apparatus and said peripheral device, said message controlling causing said peripheral

device to transmit a said test signal pattern from said analog output.

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3. (currently amended) The method of Claim 2 wherein the step of determining

enabling comprises repetitively selecting each one of said analog inputs of said video

processing apparatus to determine which one of said analog inputs receives said

transmitted test signal pattern.

4. (currently amended) The method of Claim 3 wherein more than one peripheral

device is connected to said video processing apparatus and the steps of commanding,

receiving, enabling and storing are repeated until for each one of said peripheral

devices have been processed.

5. (currently amended) The method of Claim 4 further comprising the a step of

constructing a map of the analog interconnectivity between each said peripheral device

and said video processing device.

6. (currently amended) The method of Claim 3 wherein said transmitted test

signal pattern is an analog video blanking signal.

7. (original) The method of Claim 1 wherein said video processing apparatus is a

digital television.

8. (original) The method of Claim 1 wherein said video processing apparatus is a

digital set-top box.

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9. (currently amended) The method of Claim 4 2 wherein said digital bus is an

IEEE 1394 data bus.

10. (currently amended) A method for defining the interconnectivity of a plurality

of peripheral devices to a plurality of analog inputs of a video processing apparatus,

said peripheral devices also being interconnected via a digital bus to said video

processing apparatus, said video processing apparatus performing the steps of:

(a) selecting one of said plurality of peripheral devices;

(b) sending a command, via said digital bus, to said selected peripheral device

for commanding said selected peripheral device to transmit an analog a test signal

pattern from an analog output of said selected peripheral device;

(c) receiving said analog test signal pattern from said selected peripheral device

on one of said analog inputs of said video processing apparatus, wherein said video

processing apparatus is capable of distinguishing said test signal pattern from other

signals received on said analog inputs;

(d) monitoring each of said plurality of analog inputs to determine which of said

plurality of analog inputs receives said analog test signal pattern; and

(e) repeating steps (a), (b), (c) and (d) for each of the other ones of said plurality

of peripheral devices for automatically constructing a map of the analog

interconnectivity of each said peripheral device connected to said video processing

apparatus.

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11. (original) The method of Claim 10 wherein said digital bus is an IEEE 1394

serial data bus.

12. (currently amended) A method for configuring a video processing apparatus

having an a plurality of analog inputs and being interconnected via a digital bus to at

least first and second peripheral devices, said method comprising:

(a) sending a first command, via said digital bus, to said first peripheral device to

switch said first peripheral device into a pass through operating mode:

(b) sending a second command, via said digital bus, to said second peripheral

device to transmit an analog a test signal pattern from an analog output of said second

peripheral device;

(c) receiving said analog test signal pattern from said second peripheral device

on one of said analog inputs of said video processing apparatus, wherein said video

processing apparatus is capable of distinguishing said test signal pattern from other

signals received on said analog inputs; and

(d) monitoring each of said analog inputs to determine which one of said analog

inputs receives said analog test signal pattern.

13. (original) The method of Claim 12 wherein said digital bus is an IEEE 1394

serial data bus.